

REMARKS/ARGUMENTS

Claims 1, 3-6 stand rejected under 35 U. S. C. 103 (a) as being unpatentable over Mason, Jr, et al (2003/0135674) in view of DeKoning et al. (6,216,199).

The Examiner states that Mason, JR. et al, do not teach a cache memory manager.

As described in the subject patent application, a cache memory manager provides an interface between the host computer, the bank of disk drives, and the cache memory for determining for the directors whether data to be read from the disk drives, or data to be written to the disk drives, resides in the cache memory. Thus, one cache memory manager is provided for the plurality of directors.

DeKoning deals with only one cache, one cache processor, and one storage processor. It is respectfully submitted that there is no recognition or suggestion in DeKoning to have one cache memory managers for the **plurality** of directors.

In the event a petition for extension of time is required by this paper and not otherwise provided, such petition is hereby made and authorization is provided herewith to charge deposit account No. 05-0889 for the cost of such extension.

In the event any additional fee is required, please charge such amount to Patent and Trademark Office Deposit Account No. 05-0889.

Respectfully submitted,

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Date

/richard sharkansky/

Richard M. Sharkansky Reg. No. 25,800

Attorney for Applicant(s)

P. O. Box 557

Telephone: (508) 477-4311

Facsimile: (508) 477-7234